

Notice of References Cited

Application/Control No. 10/060,156	Applicant(s)/Patent Under Reexamination MIELKE, DAVID J.		
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NON-PATENT DOCUMENTS

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	U	Schimpfle et al., "HIGH-LEVEL CIRCUIT MODELING FOR POWER ESTIMATION," IEEE, 1999, PP. 807-810.
	V	M. R. Frerichs, "PRECISE EXTRACTION OF ULTRA DEEP SUBMISSION INTERCONNECT PARASITIC WITH PARAMETERIZED 3D-MODELING," IEEE, 2/2/2001, PP. 50-55.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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